

DETAILED ACTION

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it **MUST** be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr. Christopher K. Agrawal on 01/28/10.

2. The application has been amended as follows:

IN THE CLAIMS:

Claim 38, line 2, the limitation "the size" has been replaced by --- having a size ---

. Claim 39, line 14, the limitation "wherein data element 1 and data element n" has been replaced by --- wherein the data element 1 and the data element n ---.

Claim 41, line 14, the limitation "wherein data element 1 and data element n" has been replaced by --- wherein the data element 1 and the data element n ---.

Allowable Subject Matter

3. The following is an examiner's statement of reasons for allowance:

Regarding claims 32, 43 and 45, the prior art fails to teach a combination of the steps of:

wherein sending of the first stream and second stream is at least started approximately simultaneously, in the specific combination as recited in the claim.

Regarding claim 39, the prior art fails to teach a combination of the steps of:

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wherein data element 1 and data element n are sent approximately simultaneously, in the specific combination as recited in the claim.

Regarding claim 41, the prior art fails to teach a combination of the steps of:

wherein data element 1 and data element n are received in the data buffer approximately simultaneously, in the specific combination as recited in the claim.

Regarding claim 46, the prior art fails to teach a combination of the steps of:

wherein the digital data from the front of the memory and the digital data from the back of the memory are stored approximately simultaneously, in the specific combination as recited in the claim.

Regarding claim 47, the prior art fails to teach a combination of the steps of:

wherein the first transmitter and second transmitter are configured to at least start sending the first stream and second stream approximately simultaneously, in the specific combination as recited in the claim.

Regarding claim 48, the prior art fails to teach a combination of the steps of:

wherein the digital data from the front of the memory and the digital data from the back of the memory are received approximately simultaneously, in the specific combination as recited in the claim.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to TOAN D. NGUYEN whose telephone number is (571)272-3153. The examiner can normally be reached on M-F (7:00AM-4:30PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, William Trost can be reached on 571-272-7872. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/T. D. N./
Examiner, Art Unit 2472

/William Trost/
Supervisory Patent Examiner, Art Unit 2472